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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/340,776	06/28/1999	GAJINDER SINGH PANESAR	S1022/8250	4340

7590 04/10/2002

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EXAMINER

PHAN, THAI Q

ART UNIT

PAPER NUMBER

2123

DATE MAILED: 04/10/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.
09/340,776

Applicant(s)
Gajinder Singh, anesar

Examiner
Thai Phan

Art Unit
2123



-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on Jan 30, 2002
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 35 C.D. 11; 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above, claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-7 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claims _____ are subject to restriction and/or election requirements.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are objected to by the Examiner.
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119

- 13) ☒ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).

a) ☒ All b) ☐ Some* c) ☐ None of:

1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

*See the attached detailed Office action for a list of the certified copies not received.

- 14) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

Attachment(s)

- 15) ☒ Notice of References Cited (PTO-892) 18) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 16) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 19) ☐ Notice of Informal Patent Application (PTO-152)
- 17) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s). _____ 20) ☐ Other: _____

DETAILED ACTION

This Office Action is responsive to patent application S/N: 09/340,776. Claims 1-7 are pending in this official action.

Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file. However, the printed application number in the submitted paper does not match with the number as in the Declaration. Correction is required

Drawings

2. The formal drawing, filed on Aug. 16, 1999 have been received and entered.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

4. Claims 1-7 are rejected under 35 U.S.C. 102(e) as being anticipated by Aleksic et al.,

Patent no. 5,995,736.

As per claim 1, Aleksic anticipated method and system for simulating integrated circuit including ASIC design or application specific processor (ASP) identical to the claimed invention

(Abstract). According to Aleksic, the simulation method includes steps of defining functional model for the processor and peripheral (col. 4, line 1-53), generating interface function file for each peripheral which defines the communication of the peripheral with the processor, simulating in the high level language as part of the functional model an application executable by the CPU and operation of the set of peripheral devices, including for a predetermined simulation phase, test function files, and operation of the set of peripheral devices, outputting the state of the application and the state of the peripherals to the modeling file for converting the modeling file in the high level programming language to a language executable by the simulation system (Figs. 2-4, col. 4, lines 48-65, col. 5, lines 18-31, col. 6, lines 15-35, cols. 7-8), and simulating the application specific integrated circuit.

As per claim 2, Aleksic anticipated environment stimuli for peripherals.

As per claim 3, Aleksic anticipated programming language being used to program simulation program which would include C as claimed.

As per claim 4, Aleksic anticipated description language (38) for functional simulation such as HDL as known for those skilled in the art (col. 2, lines 45-56).

As per claim 5, Aleksic anticipated a plurality of processors or microprocessor in broad sense to carry out ASIC design simulation (col. 1, "Background of the Invention").

As per claim 6, claim 6 is directed to system for performing steps of method claim 1 above, and Aleksic also anticipated system including processor means as claimed for performing steps of simulating a functional model (col. 2, lines 45-56, col. 5, lines 18-31), means for converting result of hdl functional model simulation into a simulation language such as C/C++, for

instance (col. 5, line 8 to col. 6, line 6, line 56) and means for simulating the circuit design at circuit level for subsequent simulation phase (col. 5, lines 37-54, col. 7, lines 40-60, col. 8, lines 8-60). Claim 6 is thus rejected in like manner.

As per claim 7, Aleksic anticipated computer program including modeling file as claimed for performing steps as in method claims 1 and 6 above; therefore, claim 7 is also rejected in like manner.

Response to Arguments

5. Applicant's arguments filed 01/30/2002 have been fully considered but they are moot in view of a new ground of rejection.

Conclusion

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thai Phan whose telephone number is (703) 305-3812.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (703)305-3900.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

or faxed to:

(703) 308-9051, (for formal communications intended for entry)

Or:

(703) 308-1396 (for informal or draft communications, please label "PROPOSED"

or "DRAFT")

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive,
Arlington, VA., Sixth Floor (Receptionist).

April 1, 2002



KEVIN J. TESKA
SUPERVISORY
PATENT EXAMINER